



Europäisches Patentamt  
European Patent Office  
Office européen des brevets



Publication number: **0 571 969 A2**

(12)

## EUROPEAN PATENT APPLICATION

(21) Application number: 93108484.2

(51) Int. Cl.<sup>5</sup>: G06F 15/66

(22) Date of filing: 26.05.93

(30) Priority: 26.05.92 JP 160392/92

(43) Date of publication of application:  
01.12.93 Bulletin 93/48

(84) Designated Contracting States:  
DE FR GB

(71) Applicant: Dainippon Screen Mfg. Co., Ltd.  
1-1, Tenjinkitamachi  
Teranouchi-Agaru 4-chome  
Horikawa-Dori  
Kamikyo-ku Kyoto 602(JP)

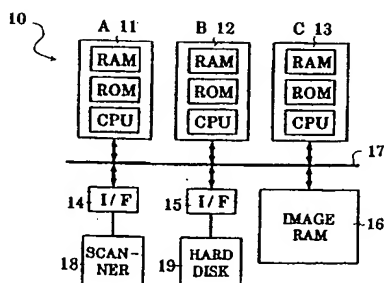
(72) Inventor: Yamaguchi, Katsuya, Dainippon  
Screen Mfg. Co. Ltd.  
1-1 Tenjinkitamachi,  
Teranouchi-agaru 4-chome  
Horikawa-dori, Kamikyo-ku, Kyoto(JP)

(74) Representative: WILHELMS, KILIAN &  
PARTNER Patentanwälte  
Eduard-Schmid-Strasse 2  
D-81541 München (DE)

(54) Image data processor and image data processing method.

(57) An image data processor including a plurality of unit processors and processes a block of image data using the unit processors in parallel. Allotment ratios corresponding to the unit processors are stored in an allotment memory, and each of the unit processors processes its allotment of the block of image data according to its allotment ratio, whereby the lengths of time needed to process the allotments are equalized. The allotment ratios can be updated from time to time regarding the actual results of the processing time of the unit processors, or regarding the content of the image (which can be detected by sampling a part of the image data) and the type of data processing.

Fig. 1



EP 0 571 969 A2

The present invention relates to an image data processor and image data processing method which includes a plurality of unit processors and divides a block of image data into allotments to process the image data in parallel.

## 5 BACKGROUND OF THE INVENTION

Since the amount of data of an image is generally very large, it takes a long time if the image data is processed sequentially. Thus, a parallel data processing is devised for decreasing the data processing time, in which a plurality of unit processors are provided, a block of data of an image is divided equally and  
10 allotted to the unit processors, and the allotments are processed in parallel by the unit processors.

If, however, the hardware constructions of the plural unit processors are diverse, their data processing abilities are also diverse. And there is a case in which one or some of the unit processors are assigned other jobs than the parallel data processing job. Further, since an image is hardly uniform in its complexity, the time needed to process data of an image differs from part to part of the image. Therefore, the lengths of  
15 processing time of plural unit processors are usually different from one another, and it often occurs that one or some of the unit processors are still working while others have finished the assigned job if the image data is divided equally. In this case, the parallel data processing is not really efficient, because, if the whole of the image data is the object of the next data processing step, the next data processing step must wait until the last of the unit processors finishes its allotment.

Japanese laid-open (unexamined) patent application No. S62-166471 discloses an improved parallel data processing method in which an image is divided into many narrow linear areas, and every unit processor sequentially takes up another narrow area when it finishes processing an area. By this method, the processing time is averaged among the unit processors and the overall efficiency is improved.

There is a drawback in the method, and sometimes the efficiency is deteriorated. When the type of  
25 image data processing is such that processing of a part requires reference to the processing of another part (e.g., line-thickening or shading), the narrow division increases the chance of inter-part references and multiple-part references. These cross references between the plural unit processors increase the complexity of the data processing and elongate the processing time a lot.

## 30 SUMMARY OF THE INVENTION

The present invention addresses the problem about the parallel data processing of an image and provides a simple image data processing apparatus and method which use a plurality of unit processors in parallel and can process image data with high efficiency.

35 According to the present invention, an image data processor includes:

an image memory for storing data of an image;

an allotment memory for storing data of allotment ratios; and

a plurality of unit processors each for performing the same data processing in parallel with the other unit processors on an allotment of the image data, the allotment being determined by the allotment ratio.

40 The image data processor may further include first updating section and/or second updating section. The first updating section measures lengths of time needed for the unit processors to perform the data processing, and updates the allotment ratios according to the measured lengths of time to equalize the lengths of time of all the unit processors. The second updating section samples a part of the image data, and updates the allotment ratios according to the content of the sampled data and type of the data  
45 processing to equalize the lengths of time of all the unit processors.

An image data processing method according to the present invention includes the following steps, where data of an image stored in an image data memory are processed using a plurality of unit processors:

determining allotment ratios of the unit processors for equalizing lengths of time needed for the unit processors to perform the image data processing;

50 calculating a starting address of the image data memory for each of the unit processors according to the allotment ratios; and

reading out data, at each of the unit processors, from the image data memory from the starting address corresponding the unit processor and processing data in parallel with the other unit processors.

Details of the present invention is described with other features of the present invention in the following  
55 description of preferred embodiments.

## BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram of an image data processor embodying the present invention.

Fig. 2 is a diagram showing a division of a block of image data.

5 Fig. 3 is a diagram showing an allotment data memory.

Fig. 4 is a timing chart of various signals for controlling the traffic of image data in the bus line of the image data processor of the embodiment.

Fig. 5 is a block diagram of another image data processor embodying the present invention.

10 Fig. 6A is a sample image, and Fig. 6B shows a data sampling and an allotment plan on the sample image.

## DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

The first embodiment of the present invention is described referring to Figs. 1 to 4. As shown in Fig. 1, the image data processor 10 of the present embodiment includes an image RAM 16, three unit processors A11, B12 and C13 for processing in parallel the data in the image RAM 16, and a bus line 17 connecting those elements. The data processor 10 is further connected to an input scanner 18 and an external memory device (hard disk) 19 via respective interfaces (I/F) 14 and 15.

Every unit processor A11, B12 or C13 includes a CPU, ROM and RAM, and reads out image data from the image RAM 16, processes the image data according to a preset program and writes back the processed image data into another area (or overwrites on the same area in some cases) of the image RAM 16. Traffic of the image data between the unit processors A11, B12 and C13 and the image RAM 16 in the bus line 17 is controlled by, as shown in Fig. 4: a clock signal CLK; time slot allotting signals  $T_A$ ,  $T_B$  and  $T_C$  respectively corresponding to the unit processors A11, B12 and C13; a data valid signal; and a read/write (R/W) signal. The time slot allotting signals  $T_A$ ,  $T_B$  and  $T_C$  change their state at every predetermined cycles of the clock signal CLK and give a bus appropriation right to the three unit processors A11, B12 and C13 cyclically. Every unit processor A11, B12 or C13 reads image data from the image RAM 16 or writes processed image data to the image RAM 16 according to the state of the R/W signal within the allotted time slot. The image RAM 16 receives data from the bus line 17 when the data valid signal indicates that the data on the bus line 17 is valid.

Since, in the image data processor 10 of the present embodiment, the unit processors A11, B12 and C13 have the same CPU and the same hardware construction, the data processing abilities of the unit processors A11, B12 and C13 are the same. But one of the unit processors (A11) is special in the present embodiment because it is assigned additional jobs (control of the input scanner 18 and the hard disk 19, and an adjustment among all jobs of the image data processor 10) besides the job of the parallel image data processing assigned to the unit processor A11 as well as to the other two unit processors B12 and C13. The unit processor A11 performs such additional jobs for 0.05 seconds at every 0.1 seconds, which means that the unit processor A11 has a heavier burden than the other two unit processors B12 and C13, and the image data processing efficiency of the unit processor A11 is half of the other unit processors B12 or C13.

Therefore, in the image data processor 10 of the present embodiment, as shown in Fig. 2, a block of data 21 corresponding to an image is divided not equally but with different allotment ratios a, b and c (here,  $a + b + c = 1$ ) which are predetermined regarding the burdens of the unit processors A11, B12 and C13. In the present embodiment, the allotment ratio (a) of the unit processor A11 is half of the other two allotment ratios (b and c) of the other two unit processors B12 and C13 (i.e.,  $a = 0.2$ ,  $b = 0.4$  and  $c = 0.4$ ). The data 22 of the allotment ratios a, b and c are stored in an allotment data memory area of a RAM provided in every unit processor A11, B12 or C13, as shown in Fig. 3.

When a parallel image data processing is assigned, every unit processor A11, B12 or C13 reads out the data 22 of the allotment ratio from its RAM, and calculates the read-starting address (the first address to read out data) of the image RAM 16 according to the following formulae.

In the unit processor A11, [read-starting address] = [first address of the data block 21]

In the unit processor B12, [read-starting address] = [first address of the data block 21] + [amount of the data block 21][allotment ratio a].

In the unit processor C13, [read-starting address] = [first address of the data block 21] + [amount of the data block 21]

[allotment ratio a + allotment ratio b]

The three unit processors A11, B12 and C13 start parallel processing of the image data at the same time. Provided the size of the block of the image data 21 is 10000 pixels in the x axis and 10000 pixels in

the y axis, the unit processor A11 starts reading data from the first address of the area of the image RAM 16 storing the data block 21, and finishes data reading at the 2000th pixel line in the x direction. The unit processor B12 processes data from the 2001st pixel line to the 6000th pixel line, and the unit processor C13 from the 6001st pixel line to the last (10000th) pixel line. Since the allotment ratios a, b and c are determined regarding the burdens of the respective unit processors A11, B12 and C13, their processing times are equalized and they finish respective image data processing almost at the same time, whereby the efficiency of the entire image data processor 10 is enhanced.

In the above description, respective unit processors A11, B12 and C13 store the allotment data 22 and calculate the read-starting address. It is possible instead, as shown in Fig. 5, to provide another independent CPU 42 for controlling the image data processor 20 as a whole and a common RAM 41 for storing the allotment data 22 besides those provided in the respective unit processors A11, B12 and C13.

In the first embodiment described above, the hardware constructions (and thus the data processing abilities) of the three unit processors are the same but the assigned burdens are diverse. When, on the contrary, the assigned burdens are the same but the abilities of the hardware are diverse, the allotment data 22 can be predetermined similarly and stored in the RAM (of the respective unit processor A11, B12 and C13, or in the common RAM 41). For example, when an initially used unit processor A11 using a 16-bit CPU is joined by two unit processors B12 and C13 each using a 32-bit CPU to perform parallel processing, the allotment ratio of the less efficient unit processor A11 is set smaller than those of the new more efficient unit processors B12 and C13, whereby the processing time of the unit processors A11, B12 and C13 are equalized.

When the processing abilities (or burdens) of the unit processors A11, B12 and C13 are unchanged, the image data processor of the above embodiment functions properly. But when the burden of the jobs other than the parallel data processing job (e.g., the control of the input scanner 18 in the above example) changes while the unit processor A11 is processing the allotted data of the parallel processing, the finishing time of the unit processors deviates. In this case, it is possible to update the allotment ratio from time to time according to the actual result of the processing time of the unit processors. Such updating operation of the allotment ratio can be done by any of the unit processors A11, B12 or C13 in case of Fig. 1 or by the independent CPU 42 in case of Fig. 5. The second embodiment of the present invention is an image data processor which updates the allotment ratio during a parallel data processing.

An initial allotment ratios  $a_0$ ,  $b_0$  and  $c_0$  for the unit processors A11, B12 and C13 are tentatively determined regarding the hardware capabilities and known burdens of the unit processors A11, B12 and C13, and the initial allotment ratios  $a_0$ ,  $b_0$  and  $c_0$  are stored in a RAM (in the RAM of the unit processor A11 for example in case of Fig. 1 or in the common 41 RAM in case of Fig. 5). First, a parallel data processing is performed using the initial allotment ratios  $a_0$ ,  $b_0$  and  $c_0$ , and the lengths of the processing time  $T_{A1}$ ,  $T_{B1}$  and  $T_{C1}$  of the unit processors A11, B12 and C13 are measured by a CPU (the CPU of the unit processor A11 for example in case of Fig. 1 or the independent CPU 42 in case of Fig. 5). Regarding the measured lengths of the processing time  $T_{A1}$ ,  $T_{B1}$  and  $T_{C1}$  of the unit processors A11, B12 and C13, the CPU updates the initial allotment ratios  $a_0$ ,  $b_0$  and  $c_0$  to new allotment ratios  $a_1$ ,  $b_1$  and  $c_1$  using the following formulae.

$$\begin{aligned} a_1 &= (a_0/T_{A1}) / (a_0/T_{A1} + b_0/T_{B1} + c_0/T_{C1}) \\ b_1 &= (b_0/T_{B1}) / (a_0/T_{A1} + b_0/T_{B1} + c_0/T_{C1}) \\ c_1 &= (c_0/T_{C1}) / (a_0/T_{A1} + b_0/T_{B1} + c_0/T_{C1}) \end{aligned}$$

The CPU stores the new allotment ratios  $a_1$ ,  $b_1$  and  $c_1$  in the RAM. In the next round of parallel data processing, the unit processors A11, B12 and C13 determine their allotment of image data (i.e., calculate the read-starting addresses) using the new allotment ratios  $a_1$ ,  $b_1$  and  $c_1$  stored in the RAM, and processes the allotted data. After the round of parallel data processing is finished, the CPU measures the lengths of processing time of the unit processors A11, B12 and C13 again and updates the allotment ratios  $a_1$ ,  $b_1$  and  $c_1$  to second-new allotment ratios  $a_2$ ,  $b_2$  and  $c_2$ . Thus the allotment ratios  $a_i$ ,  $b_i$  and  $c_i$  are repeatedly updated regarding the actual result (processing time) of the previous round of parallel data processing, whereby the lengths of processing time of the unit processors A11, B12 and C13 are gradually equalized.

There is a case where the processing time changes according to the content of the image data. When, for example, contour emphasizing or thickening (e.g., thickening the areas of a specific color) is required for a block of image data, the image data processing time is short in the area not including a contour or area of the color, but long processing time is required in the area including a long contour or including a long boundary of such area. The third embodiment of the present invention is an image data processor that changes the allotment ratios according to the content of the image data to be processed.

An image as shown in Fig. 6A is given in which distinct areas are variously colored (in colors  $C_1$ - $C_8$ ), and the areas of color  $C_2$  are required thickening in this case. Three unit processors A11, B12 and C13 of the image data processor 10 in Fig. 1 or those of the image data processor 20 in Fig. 5 perform the thickening processing in parallel as follows. First the content of the image is reviewed briefly. That is, as shown in Fig. 6B, the image data 51 are sampled on the ten x-directional lines LX1-LX10 and on the ten y-directional lines LY1-LY10 which are evenly dispersed in the block of the image data, and detects such data that are located on the boundary between the areas of color  $C_2$  and the other areas. The number of such data is stored in the RAM (in the RAM of the unit processor A11 in Fig. 1 or in the independent RAM 41). The time needed for the data sampling and the boundary detection is far shorter than the time needed to process the whole image data.

After detecting the boundary data on the sampling lines LX1-LX10 and LY1-LY10, the number of the boundary data is counted for each of the divisions of the data block 51 allotted to the unit processors A11, B12 and C13 according to the current allotment ratios  $a_{k0}$ ,  $b_{k0}$  and  $c_{k0}$  ( $a_{k0} + b_{k0} + c_{k0} = 1$ ) (as shown in Fig. 6B). Provided the counted numbers are  $k_a$ ,  $k_b$  and  $k_c$  for the three allotments, the CPU updates the current allotment ratios  $a_{k0}$ ,  $b_{k0}$  and  $c_{k0}$  to new ratios  $a_{k1}$ ,  $b_{k1}$  and  $c_{k1}$  using the following formulae.

$$\begin{aligned} a_{k1} &= \{a_{k0} - j \cdot k_a / (k_a + k_b + k_c)\} / e \\ &= e_a / e \end{aligned}$$

$$\begin{aligned} b_{k1} &= \{b_{k0} - j \cdot k_b / (k_a + k_b + k_c)\} / e \\ &= e_b / e \end{aligned}$$

$$\begin{aligned} c_{k1} &= \{c_{k0} - j \cdot k_c / (k_a + k_b + k_c)\} / e \\ &= e_c / e \end{aligned}$$

where

$$e_a + e_b + e_c = e,$$

or

$$a_{k1} = e_a / (e_a + e_b + e_c)$$

$$b_{k1} = e_b / (e_a + e_b + e_c)$$

$$c_{k1} = e_c / (e_a + e_b + e_c)$$

In the above formulae,  $j$  is a parameter that determines how much the allotment ratios should be changed according to the content of the image data 51. The value of the parameter  $j$  is preferably about 0.2. Incorporating such an updating system according to the content of the image data 51 in addition to the updating system as described in the above first and second embodiments, the processing time of the unit processors A11, B12 and C13 are further equalized.

In the above embodiment, the data at the boundary of areas at which the colors change are counted because the thickening is the object of the data processing. If the object of the data processing is to change the color of certain areas, the data in such areas are counted. That is, the data to be counted depends on the type of image data processing. The data sampling method (sampling on the lines LX1-LX10 and LY1-LY10 in the above embodiment) can be otherwise. It is possible to sample data from the points uniformly dispersed in the image.

#### Claims

1. An image data processor comprising:  
an image memory for storing data of an image;  
an allotment memory for storing data of allotment ratios; and  
a plurality of unit processors each for performing the same data processing in parallel with the other unit processors on an allotment of the image data, the allotment being determined by the

allotment ratio.

2. The image data processor according to claim 1, where the allotment memory is provided in each of the unit processors.
3. The image data processor according to claim 1, where the allotment memory is provided in a memory provided independently from the unit processors.
4. The image data processor according to claim 1, where the image data processor further comprises first updating means for measuring lengths of time needed for the unit processors to perform the data processing and for updating the allotment ratios according to the measured lengths of time to equalize the lengths of time of all the unit processors.
5. The image data processor according to claim 4, where the allotment memory is provided in each of the unit processors.
6. The image data processor according to claim 4, where the allotment memory is provided in a memory provided independently from the unit processors.
7. The image data processor according to claim 1, where the image data processor further comprises second updating means for sampling a part of the image data and for updating the allotment ratios according to the content of the sampled data and type of the data processing to equalize the lengths of time of all the unit processors.
8. The image data processor according to claim 7, where the allotment memory is provided in each of the unit processors.
9. The image data processor according to claim 7, where the allotment memory is provided in a memory provided independently from the unit processors.
10. The image data processor according to claim 4, where the image data processor further comprises second updating means for sampling a part of the image data and for updating the allotment ratios according to the content of the sampled data and type of the data processing to equalize the lengths of time of all the unit processors.
11. The image data processor according to claim 10, where the allotment memory is provided in each of the unit processors.
12. The image data processor according to claim 10, where the allotment memory is provided in a memory provided independently from the unit processors.
13. The image data processor according to claim 7, where the second updating means samples a part of the image data by taking data on a plurality of x-directional and y-directional lines equally dispersed in the image.
14. The image data processor according to claim 10, where the second updating means samples a part of the image data by taking data on a plurality of x-directional and y-directional lines equally dispersed in the image.
15. A method of processing data of an image stored in an image data memory using a plurality of unit processors, the method comprising the steps of:
  - determining allotment ratios of the unit processors for equalizing lengths of time needed for the unit processors to perform the image data processing;
  - calculating a starting address of the image data memory for each of the unit processors according to the allotment ratios; and
  - reading out data, at each of the unit processors, from the image data memory from the starting address corresponding the unit processor and processing data in parallel with the other unit processors.

16. The data processing method according to claim 15, where the method further comprises the steps of:  
measuring lengths of time needed for the unit processors to perform the data processing; and  
updating the allotment ratios according to the measured lengths of time to equalize the lengths of  
time of all the unit processors.

5

17. The data processing method according to claim 15, where the method further comprises the steps of:  
sampling a part of the data stored in the image data memory; and  
updating the allotment ratios according to the content of the sampled data and type of the data  
processing to equalize the lengths of time of all the unit processors.

10

15

20

25

30

35

40

45

50

55

Fig. 1

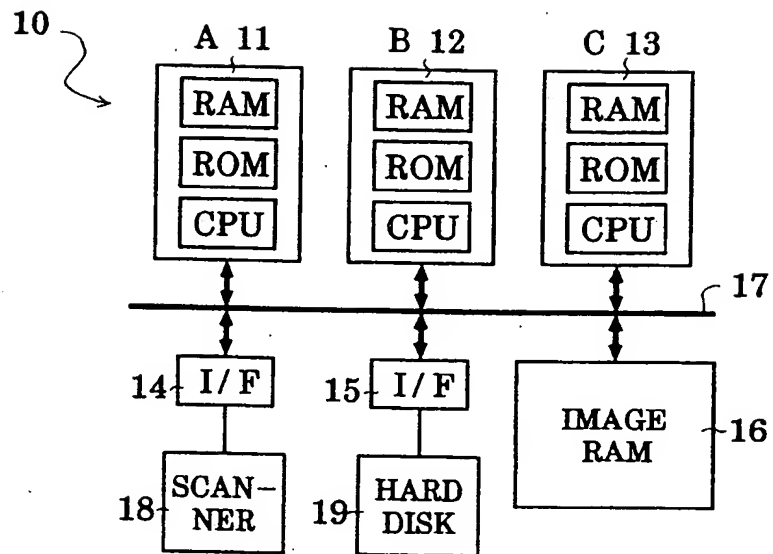




Fig. 2

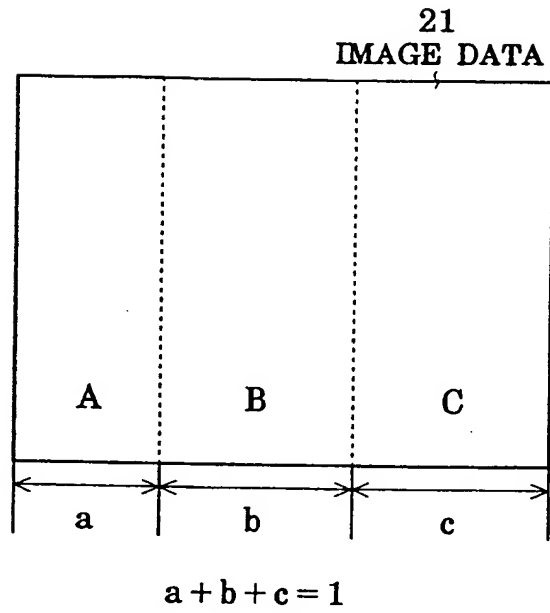


Fig. 3

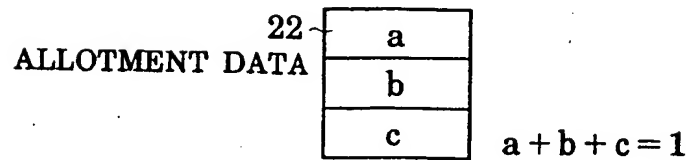


Fig. 4

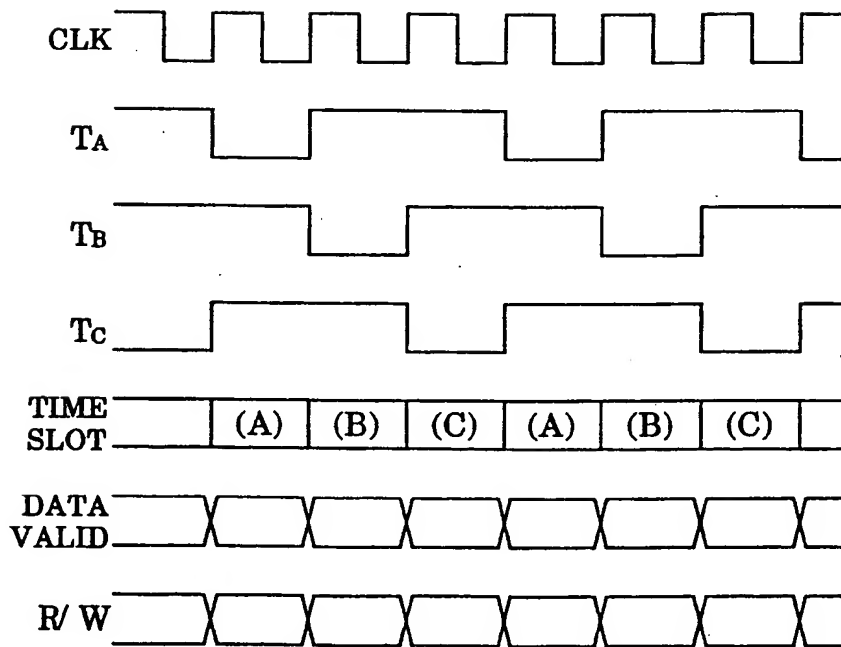


Fig. 5

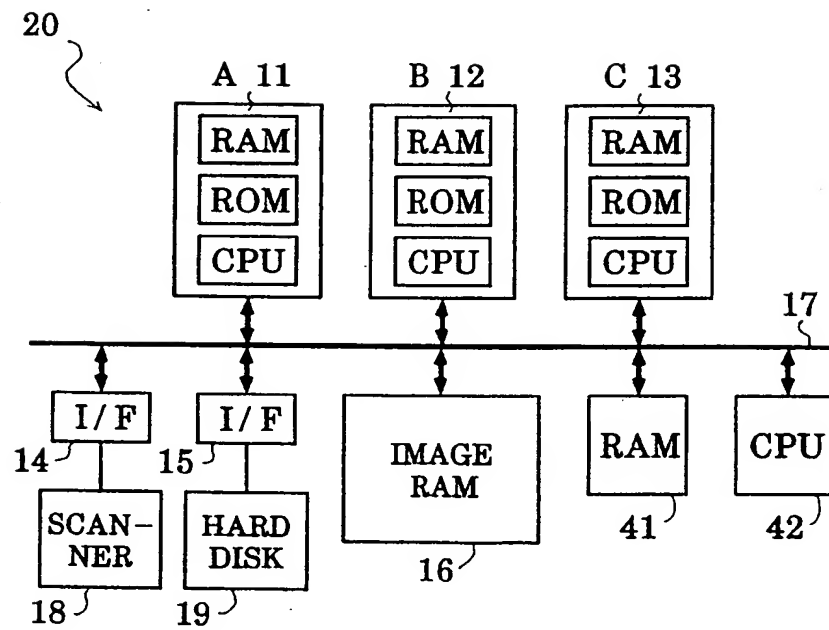


Fig. 6A

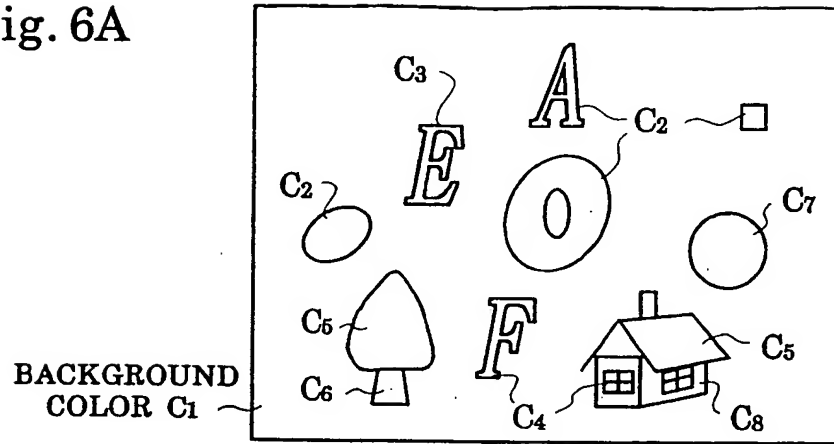
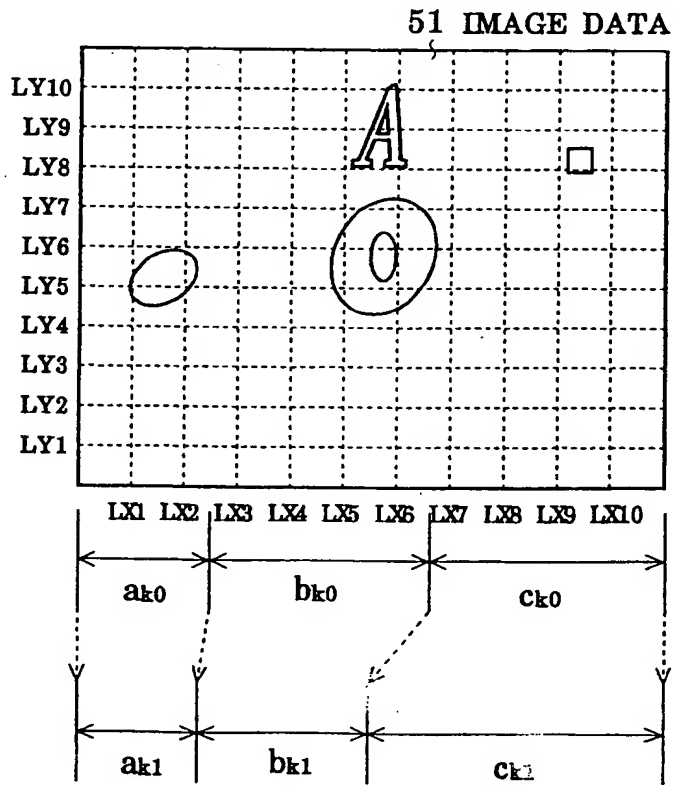


Fig. 6B





Europäisches Patentamt  
European Patent Office  
Office européen des brevets



Publication number: **0 571 969 A3**

(12)

## EUROPEAN PATENT APPLICATION

(21) Application number: 93108484.2

(51) Int. Cl.<sup>6</sup>: G06F 15/66

(22) Date of filing: 26.05.93

(30) Priority: 26.05.92 JP 160392/92

(43) Date of publication of application:  
01.12.93 Bulletin 93/48

(84) Designated Contracting States:  
DE FR GB

(86) Date of deferred publication of the search report:  
17.05.95 Bulletin 95/20

(71) Applicant: Dainippon Screen Mfg. Co., Ltd.  
1-1, Tenjinkitamachi  
Teranouchi-Agaru 4-chome  
Horikawa-Dori

Kamikyo-ku  
Kyoto 602 (JP)

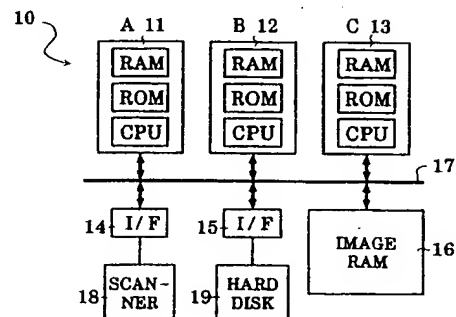
(72) Inventor: Yamaguchi, Katsuya, Dainippon  
Screen Mfg. Co. Ltd.  
1-1 Tenjinkitamachi,  
Teranouchi-agaru 4-chome  
Horikawa-dori,  
Kamikyo-ku,  
Kyoto (JP)

(74) Representative: WILHELMS, KILIAN &  
PARTNER Patentanwälte  
Eduard-Schmid-Strasse 2  
D-81541 München (DE)

(54) Image data processor and image data processing method.

(57) An image data processor including a plurality of unit processors and processes a block of image data using the unit processors in parallel. Allotment ratios corresponding to the unit processors are stored in an allotment memory, and each of the unit processors processes its allotment of the block of image data according to its allotment ratio, whereby the lengths of time needed to process the allotments are equalized. The allotment ratios can be updated from time to time regarding the actual results of the processing time of the unit processors, or regarding the content of the image (which can be detected by sampling a part of the image data) and the type of data processing.

Fig. 1



EP 0 571 969 A3



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number  
EP 93 10 8484

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.5)
X	PROCEEDINGS OF THE SPIE. EXTRACTING MEANING FROM COMPLEX DATA: PROCESSING, DISPLAY, INTERACTION., vol. 1259, February 1990 USA, pages 39-49, YUH-TAY LIOW ET AL. 'three-dimensional data visualization using DSP meshes' * page 41, paragraph 3.2.; figure 1 *	1, 2, 15	G06F15/66
X	EP-A-0 329 151 (MITSUBISHI DENKI K.K.) 23 August 1989 * claims 1,3; figures 11,15 *	1, 3-12, 15-17	
A	EP-A-0 478 338 (XEROX) 1 April 1992		
			TECHNICAL FIELDS SEARCHED (Int.Cl.5)
			G06F
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 27 March 1995	Examiner Perez Molina, ..
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons A : member of the same patent family, corresponding document	

EPO FORM 1503 (12.82) (P0601)